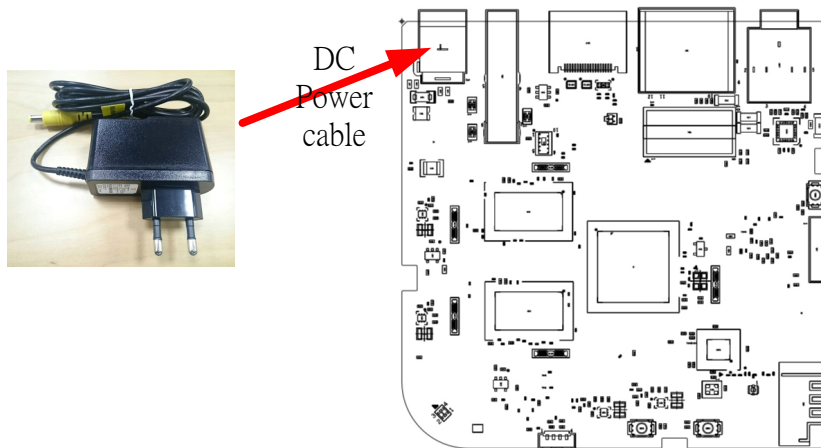


Hardware Unit Test Report

Power Sequence Test

Hardware version	FW version	Sample No.	eMMC Flash P/N
V005	4.9.113 #1MonFeb 1122:40:43 CST 2019	S/N:	KLM8G1GETF-B041

1. Power Test Setup

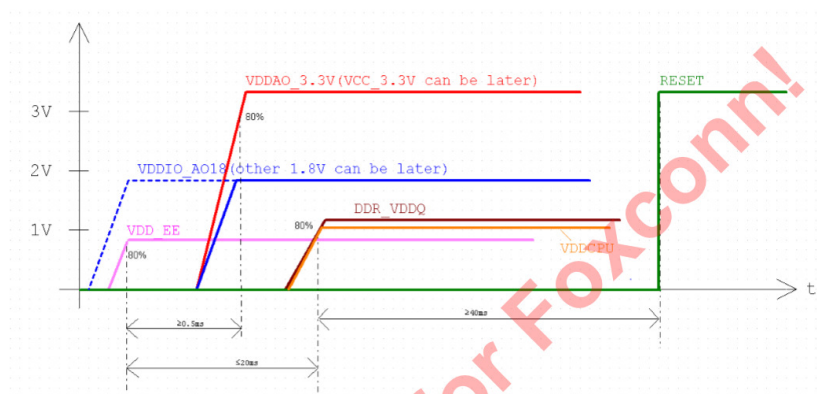


2. Test Condition

DC power: 5V 2A

3. Power on specification

Example power on sequence:



4. Power on Result and Picture

Test Item	Test Result (ms)	Figure	Sequence	Pass/Fail
5V vs VDD_EE	0.73	Figure 1.	1	Pass
5V vs P1V8	2.46	Figure 2.	2	Pass
5V vs EMMC_VDDQ	2.632	Figure 3.	3	Pass
5V vs P3V3	2.72	Figure 4.	4	Pass
5V vs P2V5	2.766	Figure 5.	5	Pass
5V vs VDDCPU	2.886	Figure 6.	6	Pass
5V vs P1V2	3.042	Figure 7.	7	Pass
5V vs CPU_RST	558.4	Figure 8.	8	Pass

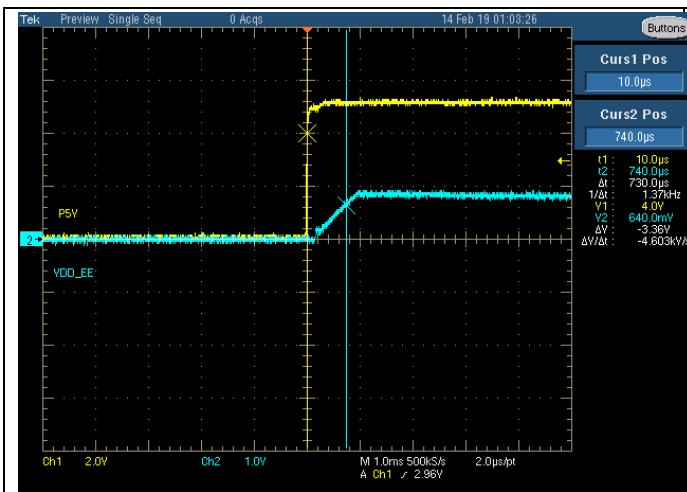


Fig. 1: 5V vs VDD_EE

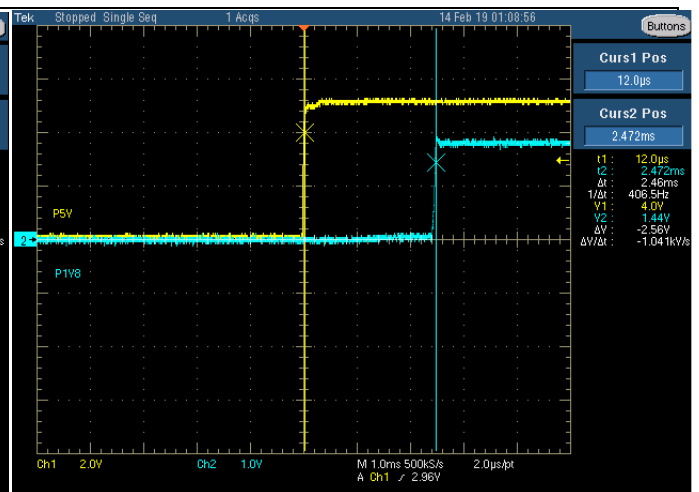


Fig. 2: 5V vs P1V8

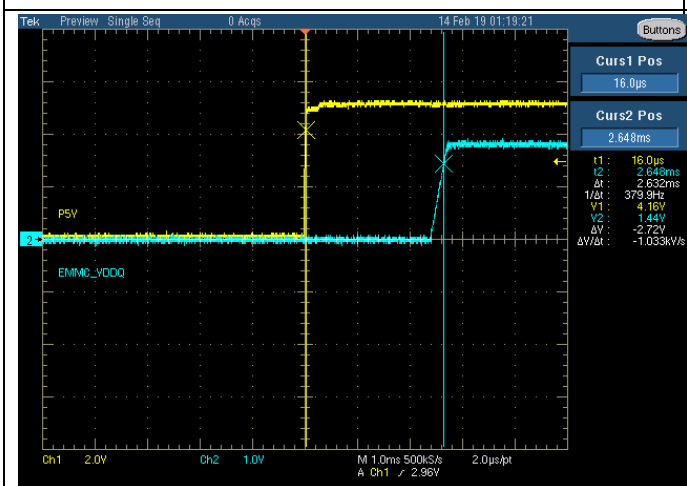


Fig. 3: P5V vs EMMC_VDDQ

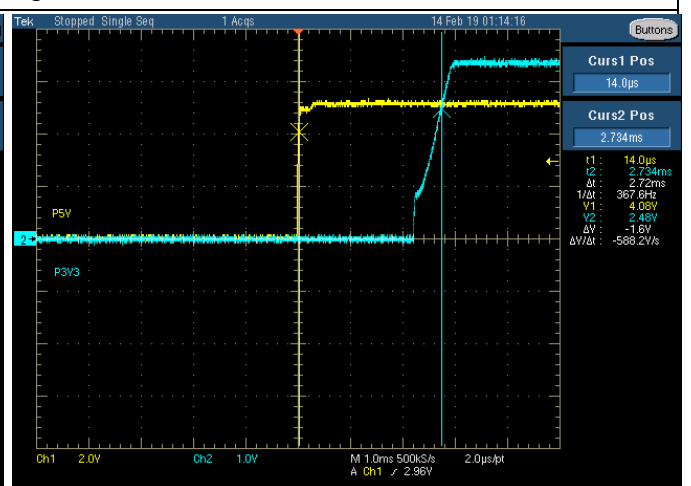


Fig. 4: P5V vs P3V3

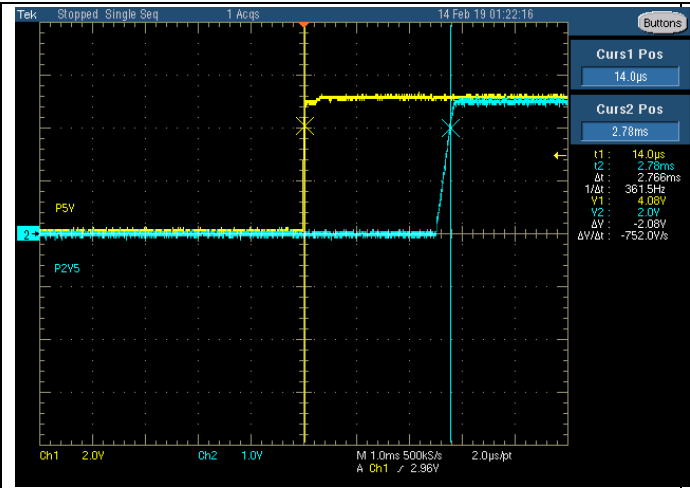


Fig. 5: P5V vs P2V5

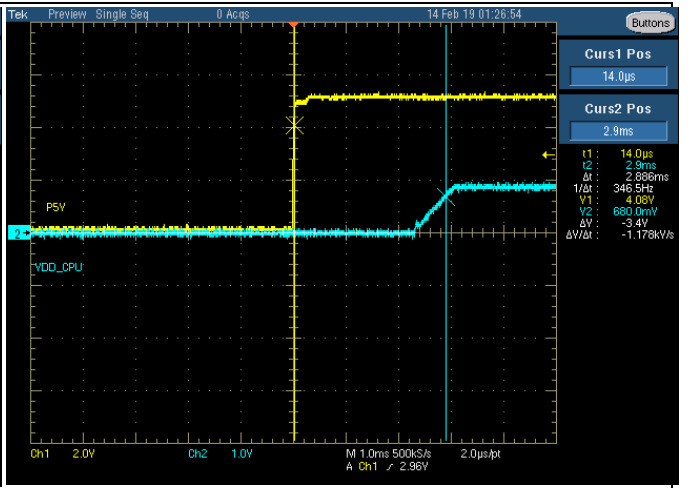


Fig. 6: P5V vs VDDCPU

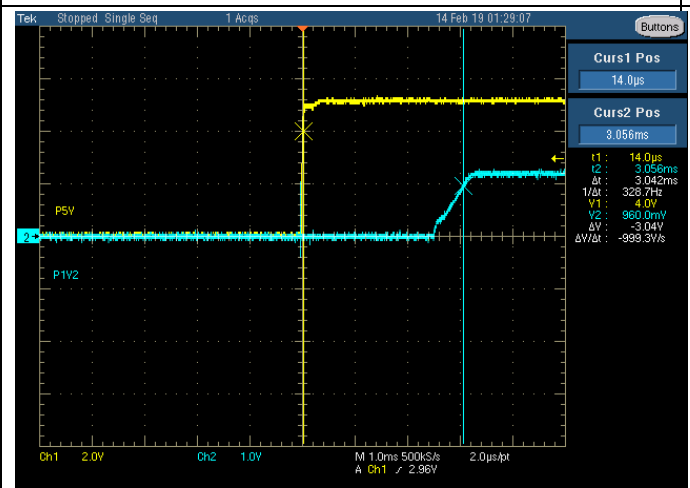


Fig. 7: P5V vs P1V2

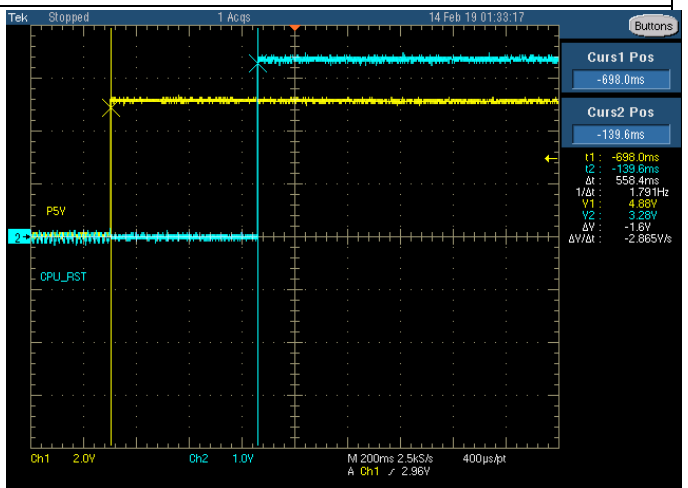


Fig. 8: P5V vs CPU_RST

5. Power off Picture

We can see that all power rail will down to 0V after power off.

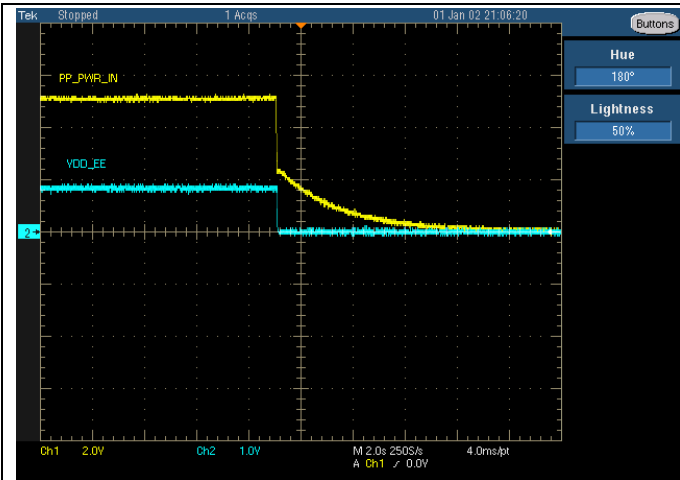


Fig. 1: 5V vs VDD_EE

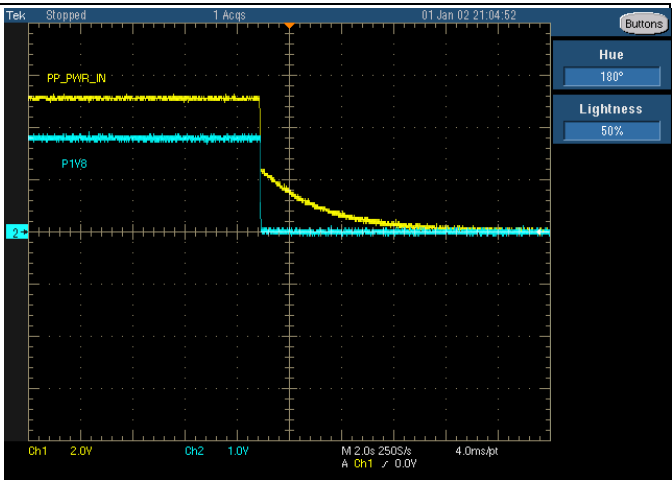


Fig. 2: 5V vs P1V8

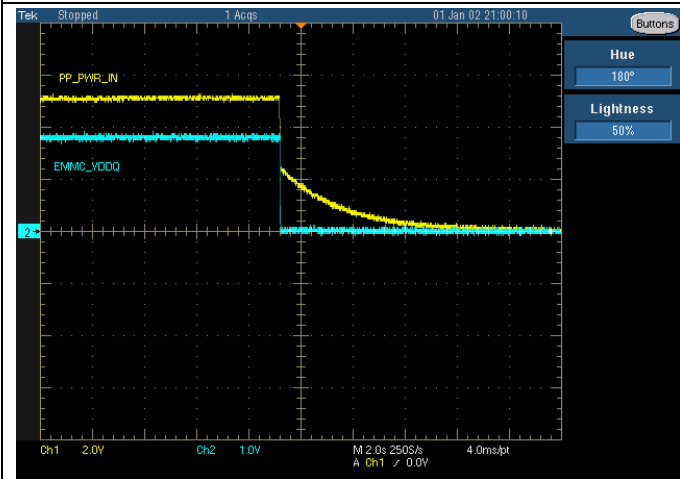


Fig. 3: P5V vs EMMC_VDDQ

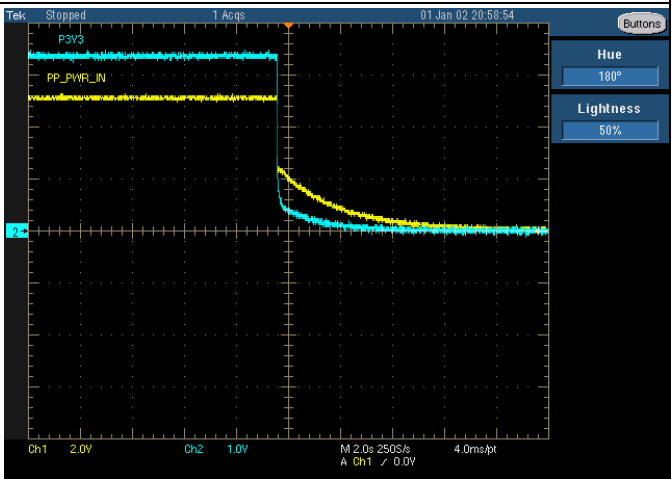


Fig. 4: P5V vs P3V3

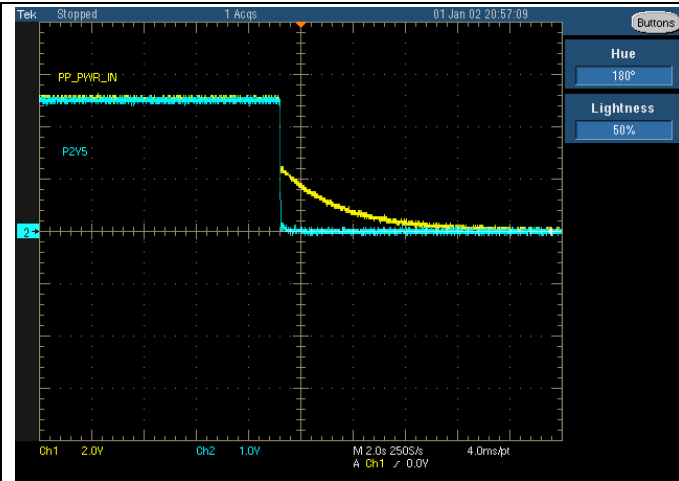


Fig. 5: P5V vs P2V5

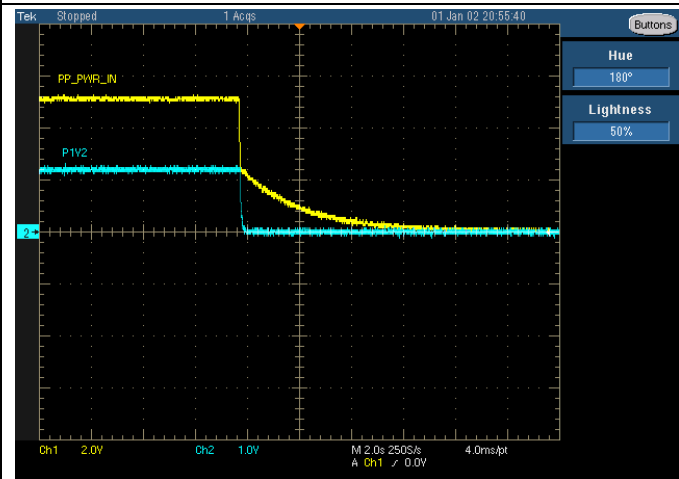


Fig. 7: P5V vs P1V2

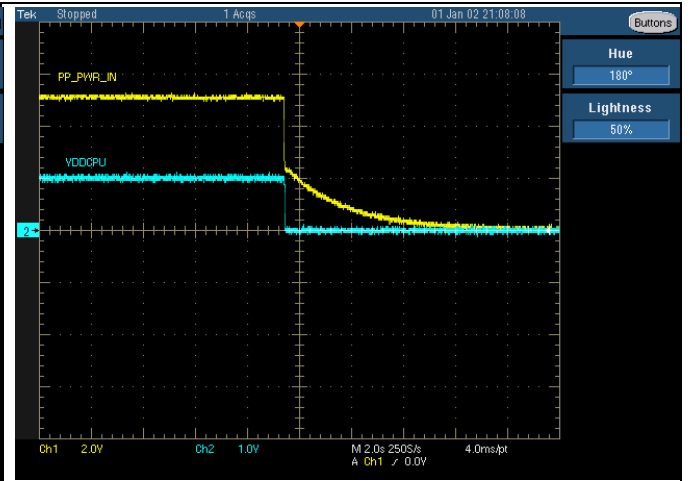


Fig. 6: P5V vs VDDCPU